



30V/138A N-Channel Power MOSFET

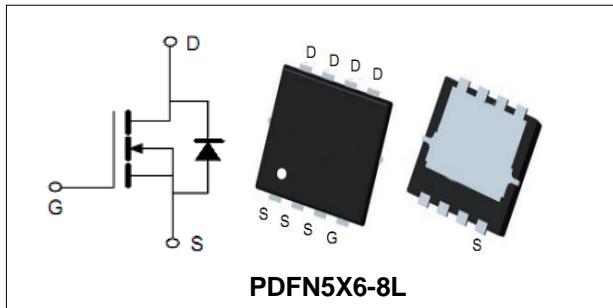
Features

- New technology for high voltage device.
- Low on-resistance and low conduction losses
- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested

BVDSS	30	V
ID	138	A
RDS(ON)@VGS=10V	1.3	mΩ
RDS(ON)@VGS=4.5V	2	mΩ

Applications

- DC/DC Converter
- Motor control and drives
- Battery management

**Order Information**

Product	Package	Marking	Reel Size	Reel	Carton
PGN03N017M	PDFN5X6-8L	PGN03N017M	13inch	5000PCS	50000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
Common Ratings (TC=25°C Unless Otherwise Noted)			
V _{(BR)DSS}	Drain-Source Breakdown Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
T _J	Maximum Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
I _S	Diode Continuous Forward Current	138	A
Mounted on Large Heat Sink			
E _{AS}	Single Pulse Avalanche Energy (Note1)	125	mJ
I _{DM}	Pulse Drain Current Tested (Sillicon Limit) (Note2)	550	A
I _D	Continuous Drain current	138	A
P _D	Maximum Power Dissipation	80	W
R _{θJC}	Thermal Resistance Junction-to-Case (Note3)	1.56	°C/W

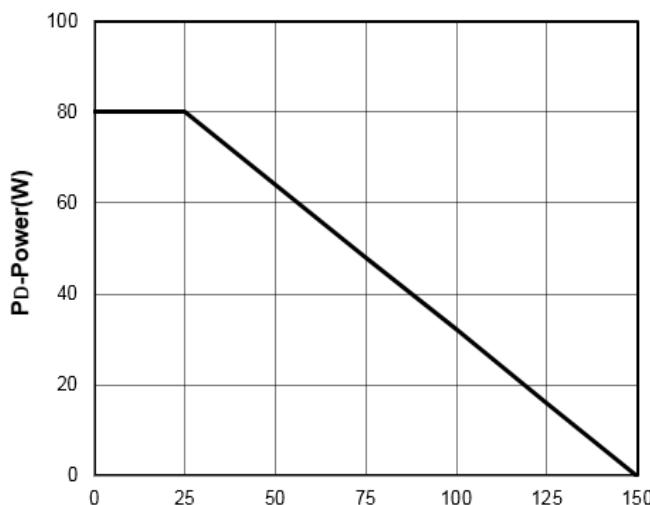
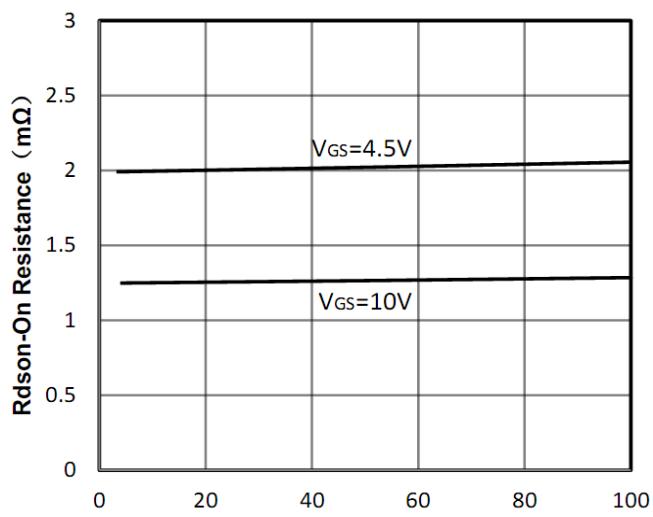
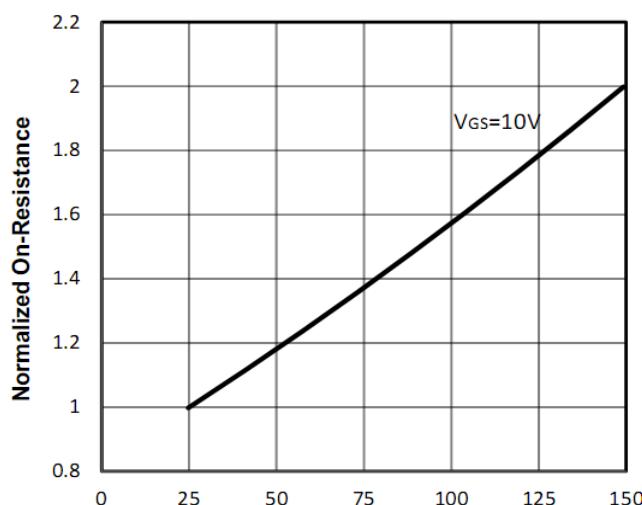
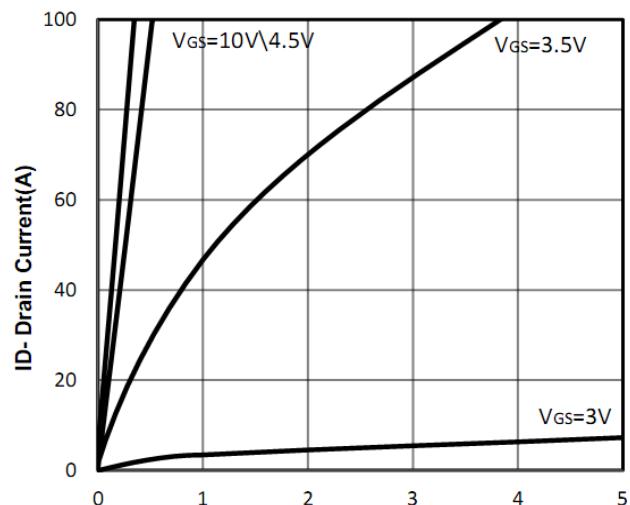
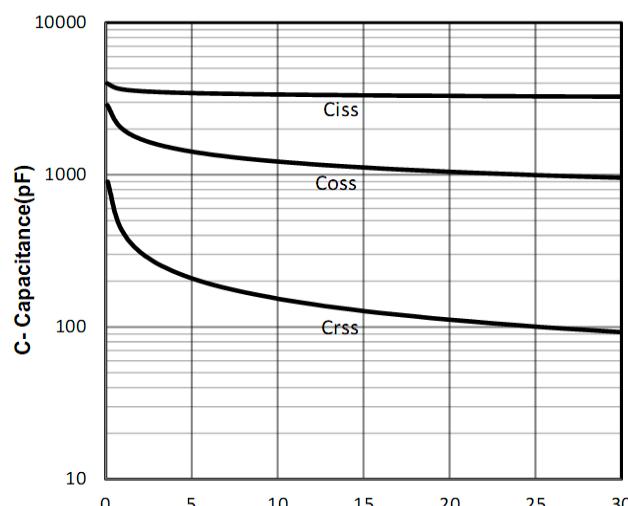
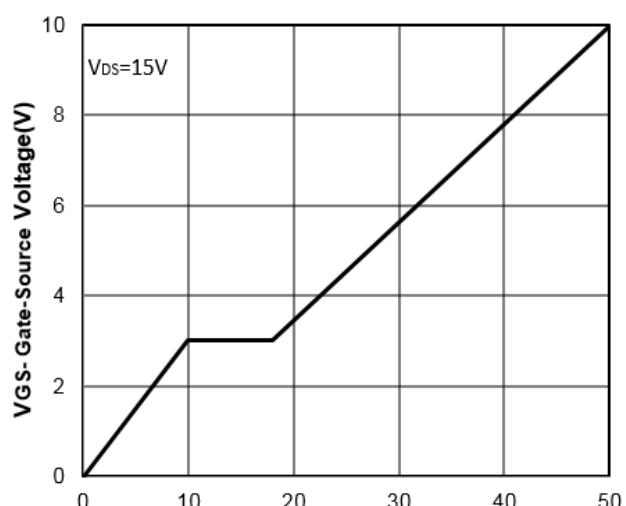


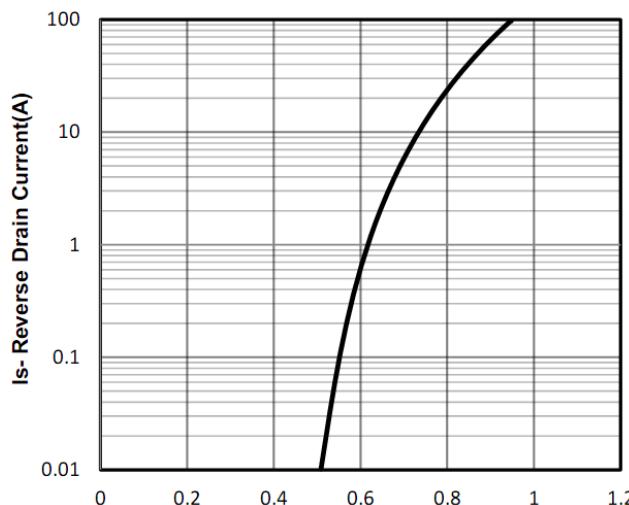
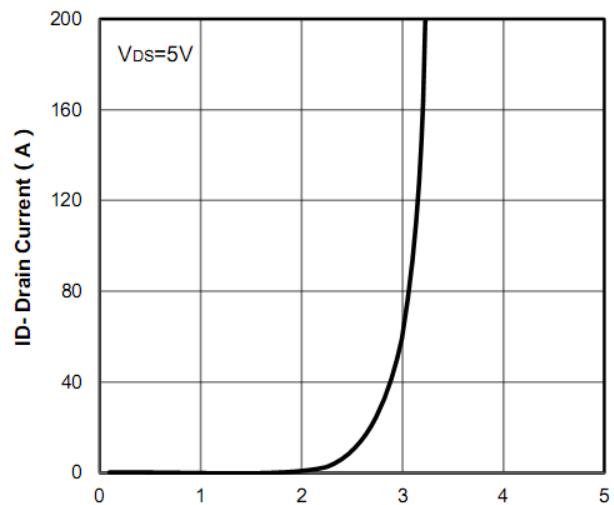
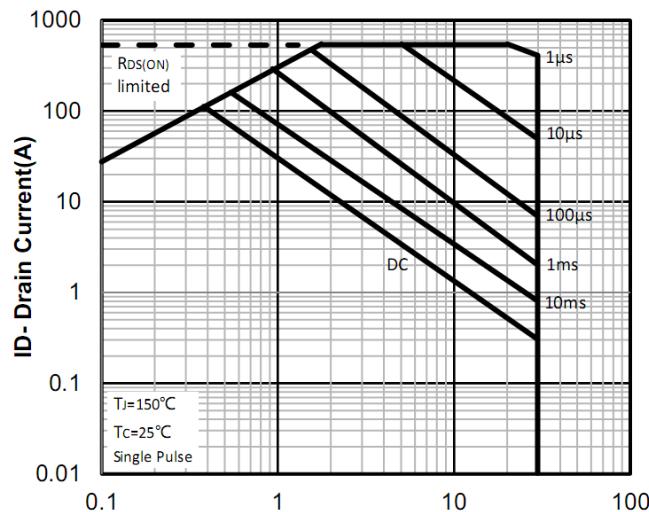
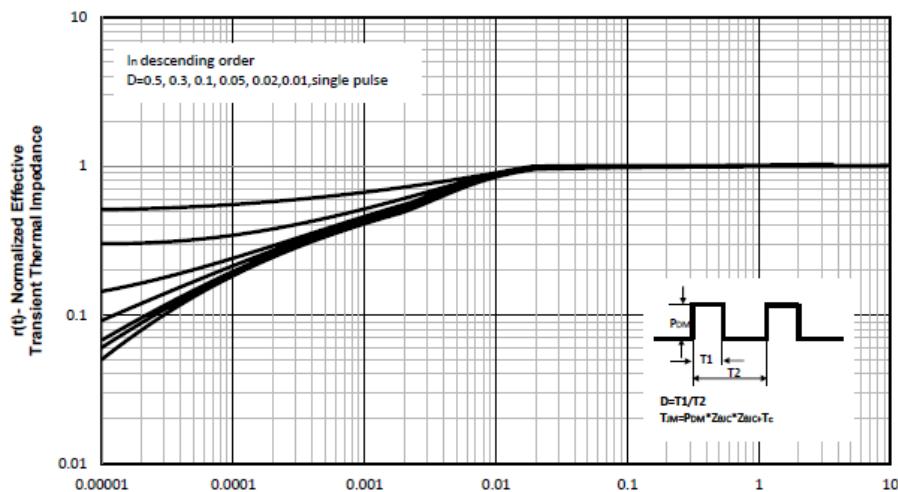
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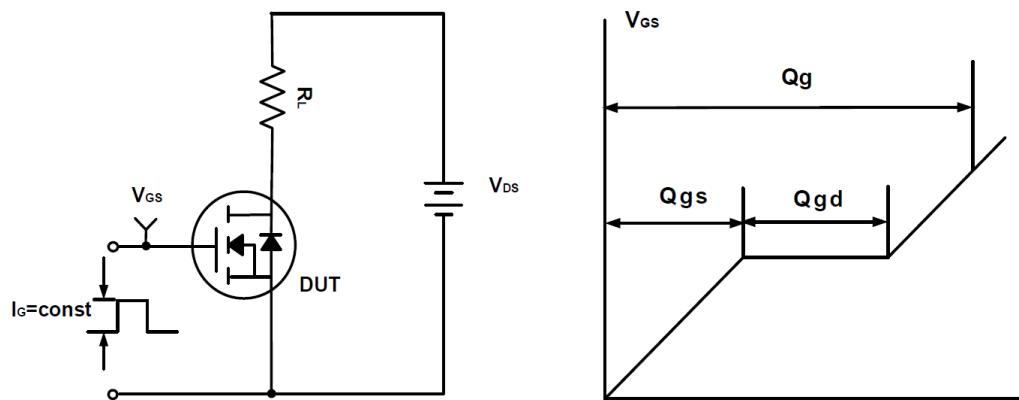
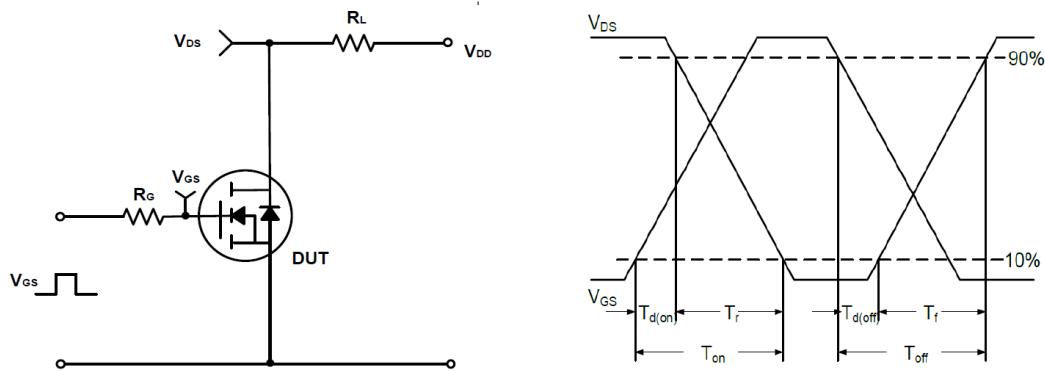
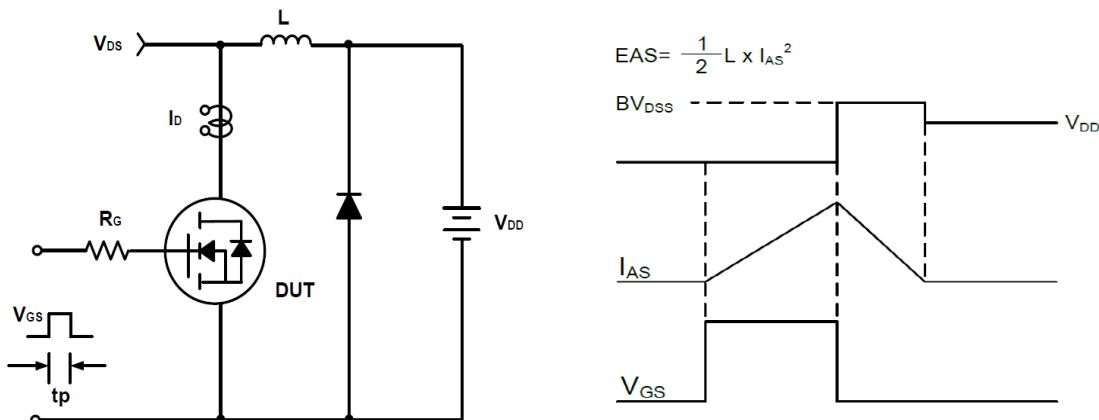
Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
$V_{(BR)DSS}$	Drain- Source Breakdown Voltage	$VGS=0V$ ID=250μA	30	--	--	V
I_{DSS}	Zero Gate Voltage Drain current	$VDS=30V, VGS=0V$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$VGS=\pm 20V, VDS=0V$	--	--	±100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$VDS=VGS, ID=250\mu A$	1.3	1.6	2.3	V
$R_{DS(ON)}$	Drain-Source On-State Resistance (Note4)	$VGS=10V, ID=20A$	--	1.3	1.7	mΩ
		$VGS=4.5V, ID=20A$	--	2	2.8	mΩ
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated) (Note5)						
C_{iss}	Input Capacitance	$VDS=15V,$ $VGS=0V,$ $F=1MHz$	--	2980	--	pF
C_{oss}	Output Capacitance		--	1050	--	pF
C_{rss}	Reverse Transfer Capacitance		--	109	--	pF
Q_g	Total Gate Charge	$VDS=15V,$ $ID=20A,$ $VGS=10V$	--	50	--	nC
Q_{gs}	Gate-Source Charge		--	10	--	nC
Q_{gd}	Gate-Drain Charge		--	8	--	nC
Switching Characteristics (Note5)						
$t_{d(on)}$	Turn-on Delay Time	$VDS=15V,$ $ID=20A$	--	10.5	--	nS
t_r	Turn-on Rise Time		--	5	--	nS
$t_{d(off)}$	Turn-off Delay Time		--	39	--	nS
t_f	Turn-off Fall Time		--	9	--	nS
Source- Drain Diode Characteristics@ TJ = 25°C (unless otherwise stated)						
V_{SD}	Forward on voltage	$ISD=2A, VGS=0V$	--	--	1.4	V

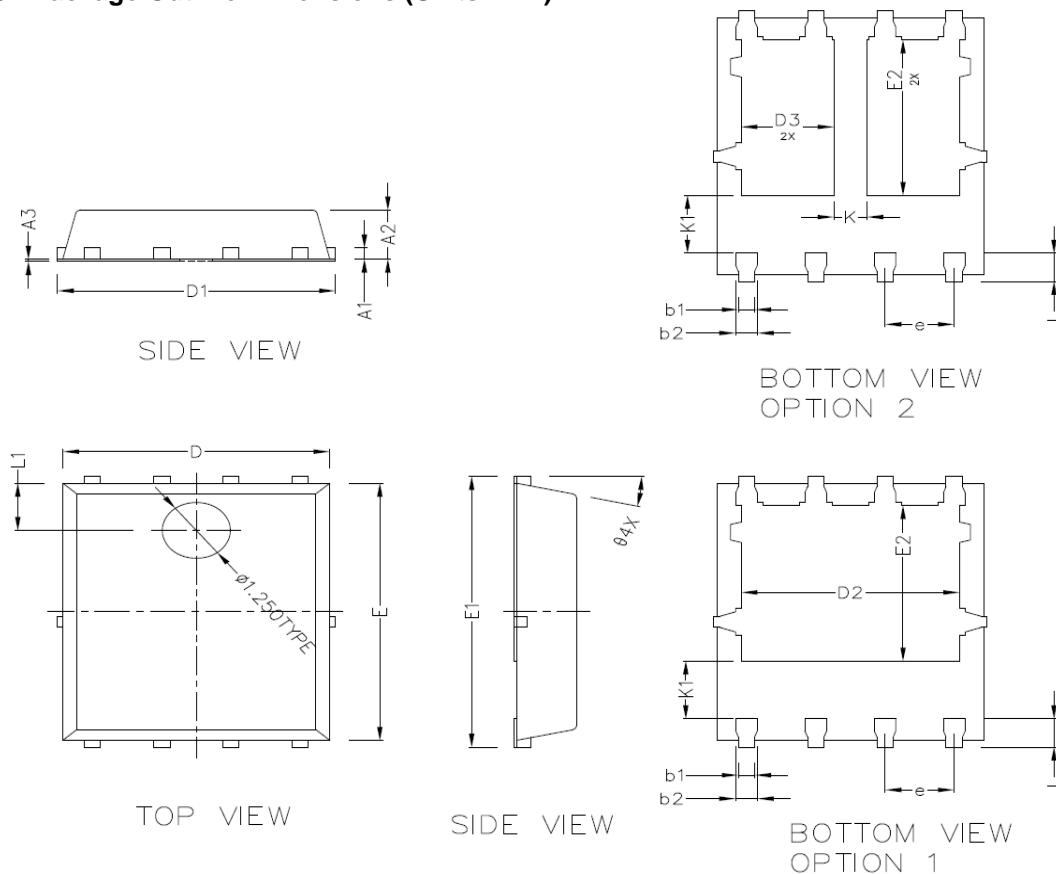
Note:

1. Limited by TJmax, starting TJ = 25° C, RG = 25Ω, VD =15V, VGS =10V. Part not recommended for use above this value.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. Surface Mounted on FR4 Board, t ≤ 10 sec.
4. Pulse Test: pulse width ≤ 300 us, duty cycle ≤ 2%.
5. Guaranteed by design, not subject to production testing.

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Typical Characteristics

Figure1: TJ- Junction Temperature (°C)

Figure2: Id- Drain Current (A)

Figure3: TJ- Junction Temperature (°C)

Figure4: VDS- Drain Source Voltage (V)

Figure5: VDS- Drain Source Voltage (V)

Figure6: Qg- Gate Charge (nC)

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Figure7: V_{sd}- Source Drain Voltage (V)

Figure8: V_{gs}- Gate Source Voltage (V)

Figure9: V_{ds}- Drain Source Voltage (V)

Figure10: Square Wave Pulse Duration (sec)

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Test Circuit and Waveform:

Figure A Gate Charge Test Circuit & Waveforms

Figure B Switching Test Circuit & Waveforms

Figure C Unclamped Inductive Switching Circuit & Waveforms

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PDFN5X6-8L Package Outline Dimensions (Units: mm)


COMMON DIMENSIONS (UNITS OF MEASURE IS			
	MIN	NORMAL	MAX
A1		0.264 BSC	
A2	1.000	1.100	1.200
A3	0.005	—	0.020
b1	0.260	0.300	0.360
b2	0.360	0.400	0.460
D	4.800	4.900	5.000
D1	5.000	5.100	5.200
D2	3.910	4.010	4.110
D3	1.605	1.705	1.805
E	5.650	5.750	5.850
E1	5.950	6.050	6.150
E2	3.375	3.475	3.575
e	1.270 TYPE		
L	0.630	0.630	0.730
L1	1.00REF		
$\frac{d}{4}$	13' TYPE		
K	0.600 REF		
K1	1.236 REF		