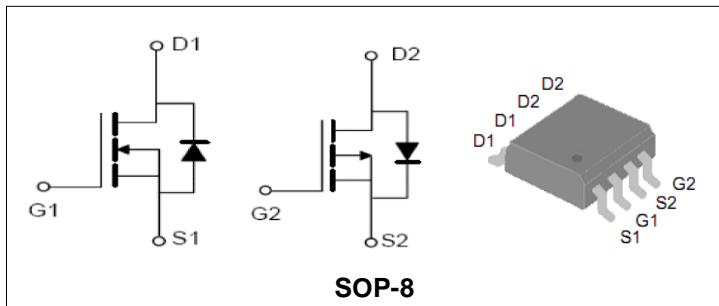


**Complementary High Density Trench MOSFET**
**Features**

- Improved dv/dt Capability, High Ruggedness
- Maximum Junction Temperature Range (150°C)

**Applications**

- PWM applications
- Load switch
- Power management



N-Channel		
BVDSS	30	V
ID	5.8	A
RDSON@VGS=10V	15	mΩ
RDSON@VGS=4.5V	20	mΩ

P-Channel		
BVDSS	-30	V
ID	-6.5	A
RDSON@VGS=-10V	27	mΩ
RDSON@VGS=-4.5V	43	mΩ

**Order Information**

Product	Package	Marking	Reel Size	Reel	Carton
PT4606	SOP-8	PT4606	13inch	3000PCS	48000PCS

**Absolute Maximum Ratings**

Symbol	Parameter	N-Channel	P-Channel	Unit	
<b>Common Ratings (TC=25°C Unless Otherwise Noted)</b>					
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	30	-30	V	
V <sub>GS</sub>	Gate-Source Voltage	±20	±20	V	
T <sub>J</sub>	Maximum Junction Temperature	150	150	°C	
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	-55 to 150	°C	
I <sub>S</sub>	Diode Continuous Forward Current TA =25°C	5.8	-6.5	A	
<b>Mounted on Large Heat Sink</b>					
I <sub>DM</sub>	Pulse Drain Current Tested (Silicon Limit) (Note1)	TA =25°C	20	-30	A
I <sub>D</sub>	Continuous Drain current	TA =25°C	5.8	-6.5	A
P <sub>D</sub>	Maximum Power Dissipation	TA =25°C	2	2	W
R <sub>θJA</sub>	Thermal Resistance Junction-to-Ambient (Note2)		63.2	63.2	°C/W



## Complementary High Density Trench MOSFET

## N-Channel Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ <math>T_J = 25^\circ C</math> (unless otherwise stated)</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$VGS=0V, ID=250\mu A$	30	--	--	V
$I_{DSS}$	Zero Gate Voltage Drain current	$VDS=30V, VGS=0V$	--	--	1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$VGS=\pm 20V, VDS=0V$	--	--	$\pm 100$	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$VDS=VGS, ID=250\mu A$	1	1.9	3	V
$R_{DS(ON)}$	Drain-Source On-State Resistance (Note3)	$VGS=10V, ID=5.8A$	--	15	31	$m\Omega$
		$VGS=4.5V, ID=5A$	--	20	43	$m\Omega$
$g_{FS}$	Forward Transconductance	$VDS=5V, ID=5A$	--	6	--	S
<b>Dynamic Electrical Characteristics @ <math>T_J = 25^\circ C</math> (unless otherwise stated) (Note4)</b>						
$C_{iss}$	Input Capacitance	$VDS= 15V,$ $VGS=0V,$ $F=1MHz$	--	398	--	pF
$C_{oss}$	Output Capacitance		--	67	--	pF
$C_{rss}$	Reverse Transfer Capacitance		--	61	--	pF
$Q_g$	Total Gate Charge	$VDS= 10V,$ $ID= 1A,$ $VGS= 10V$	--	7.4	--	nC
$Q_{gs}$	Gate-Source Charge		--	1.7	--	nC
$Q_{gd}$	Gate-Drain Charge		--	1.3	--	nC
<b>Switching Characteristics (Note4)</b>						
$t_{d(on)}$	Turn-on Delay Time	$VDD= 15V,$ $RL=15\Omega,$ $ID=1A, VGEN=10V,$ $RG=6\Omega$	--	8	--	nS
$t_r$	Turn-on Rise Time		--	11.2	--	nS
$t_{d(off)}$	Turn-off Delay Time		--	17.2	--	nS
$t_f$	Turn-off Fall Time		--	7.54	--	nS
<b>Source- Drain Diode Characteristics@ <math>T_J = 25^\circ C</math> (unless otherwise stated)</b>						
$V_{SD}$	Forward on voltage (Note3)	$IS=2.3A, VGS=0V$	--	--	1.2	V



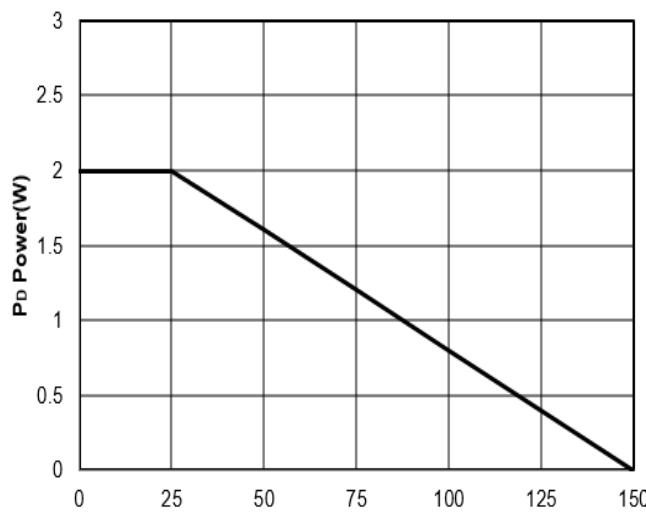
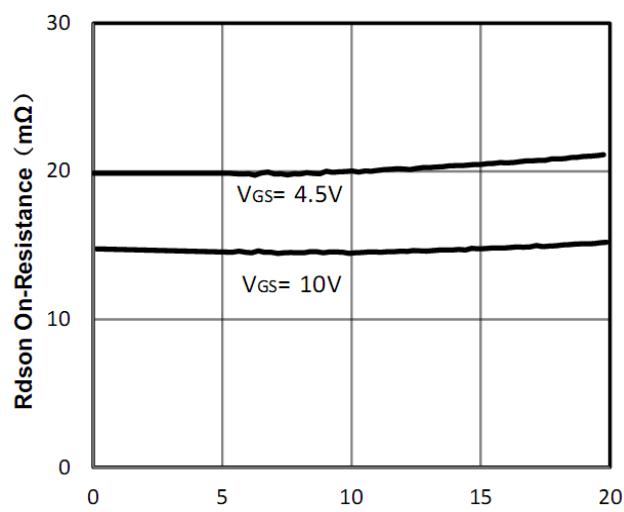
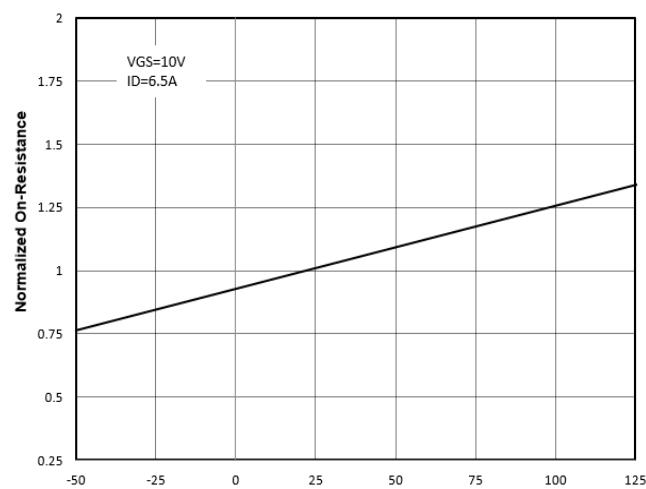
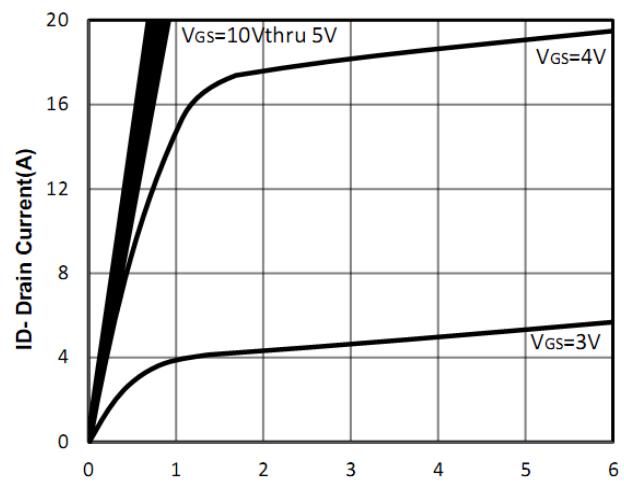
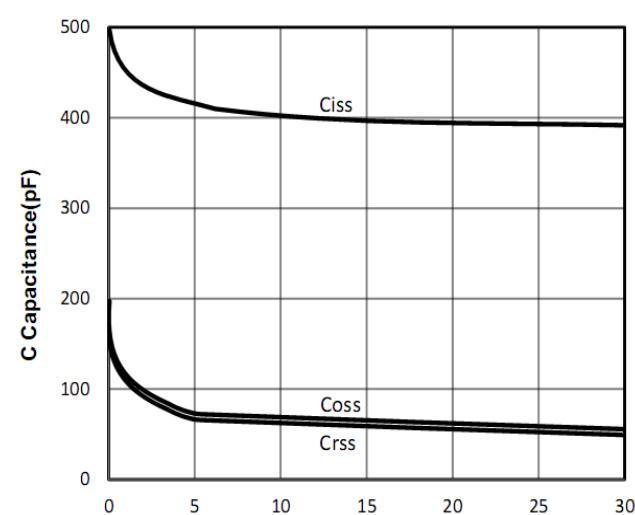
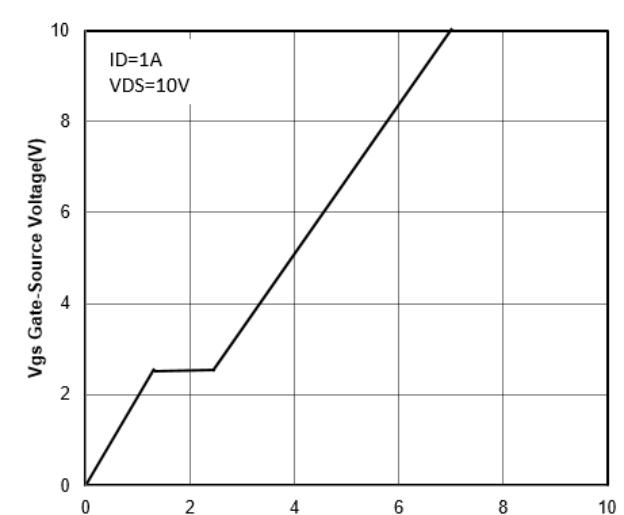
## Complementary High Density Trench MOSFET

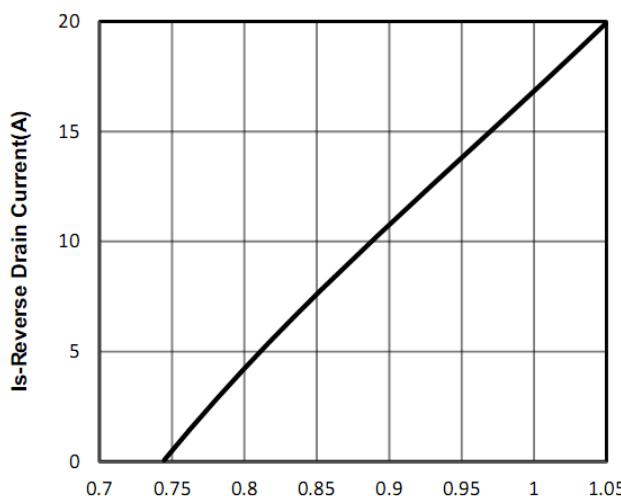
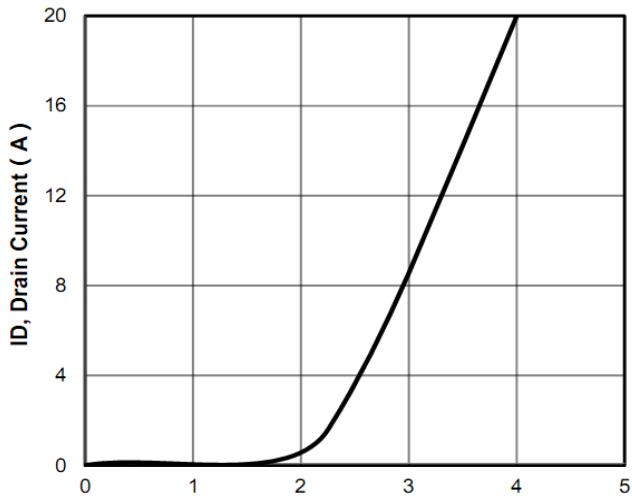
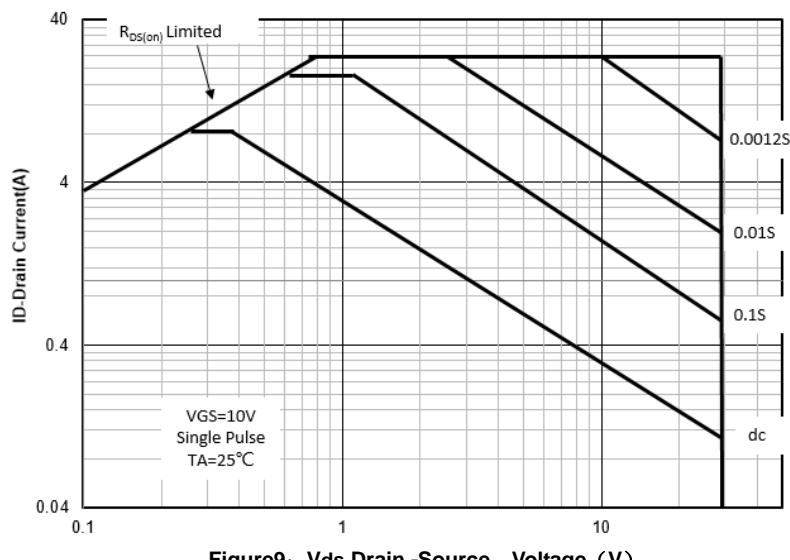
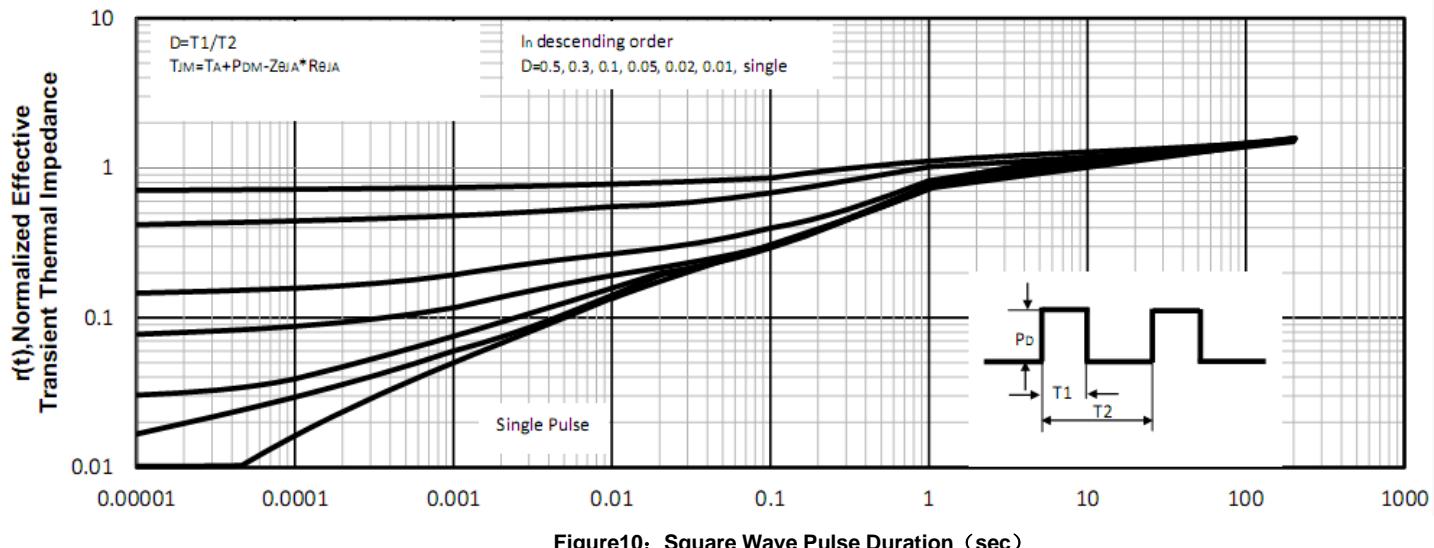
## P-Channel Electrical Characteristics

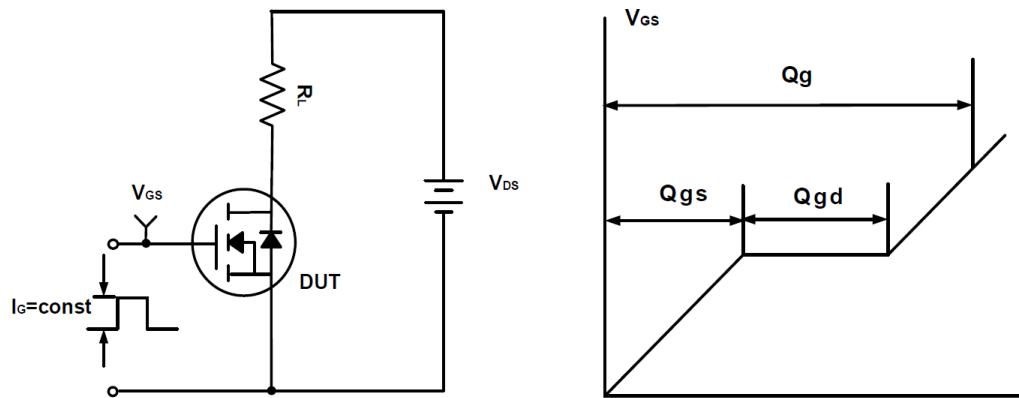
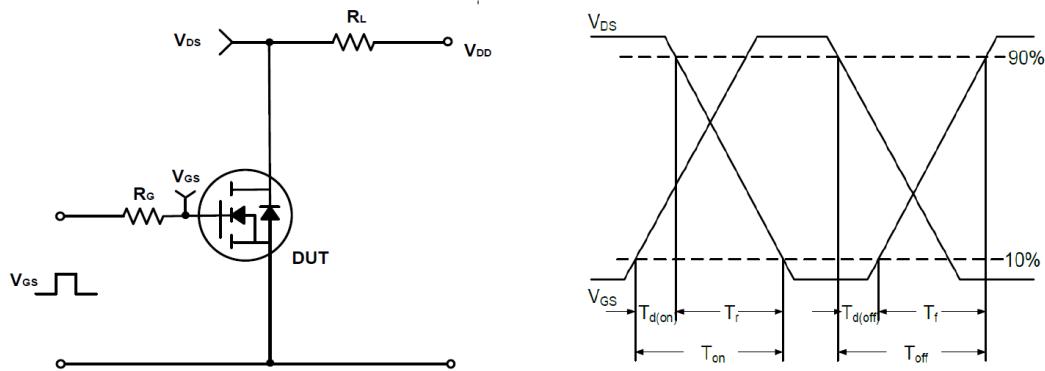
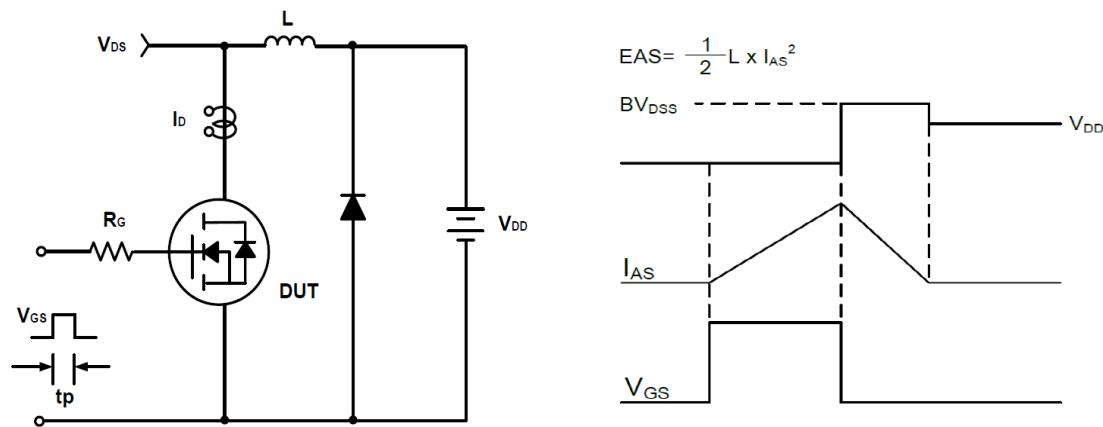
Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ <math>T_J = 25^\circ C</math> (unless otherwise stated)</b>						
$V_{(BR)DSS}$	Drain- Source Breakdown Voltage	$V_{GS}=0V$ $ID=-250\mu A$	-30	--	--	V
$I_{DSS}$	Zero Gate Voltage Drain current	$V_{DS}=-30V, V_{GS}=0V$	--	--	-1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	--	--	$\pm 100$	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, ID=250\mu A$	-1.4	-1.9	-2.4	V
$R_{DS(ON)}$	Drain-Source On-State Resistance (Note3)	$V_{GS}=-10V, ID=-6.5A$	--	27	46	$m\Omega$
		$V_{GS}=-4.5V, ID=-5A$	--	43	72	$m\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS}=-10V, ID=-6A$	--	12.7	--	S
<b>Dynamic Electrical Characteristics @ <math>T_J = 25^\circ C</math> (unless otherwise stated) (Note4)</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = -15V,$ $V_{GS}=0V,$ $F=1MHz$	--	930	--	pF
$C_{oss}$	Output Capacitance		--	121	--	pF
$C_{rss}$	Reverse Transfer Capacitance		--	102	--	pF
$Q_g$	Total Gate Charge	$V_{DS} = -15V,$ $ID= -3A,$ $V_{GS} = -10V$	--	20	--	nC
$Q_{gs}$	Gate-Source Charge		--	4.1	--	nC
$Q_{gd}$	Gate-Drain Charge		--	2.6	--	nC
<b>Switching Characteristics (Note4)</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = -15V,$ $RL=5\Omega,$ $ID=-3A, V_{GEN}=-10V,$ $RG=6\Omega$	--	9.5	--	nS
$t_r$	Turn-on Rise Time		--	5.4	--	nS
$t_{d(off)}$	Turn-off Delay Time		--	42.5	--	nS
$t_f$	Turn-off Fall Time		--	13.6	--	nS
<b>Source- Drain Diode Characteristics@ <math>T_J = 25^\circ C</math> (unless otherwise stated)</b>						
$V_{SD}$	Forward on voltage (Note3)	$IS=-1A, V_{GS}=0V$	--	--	-1	V

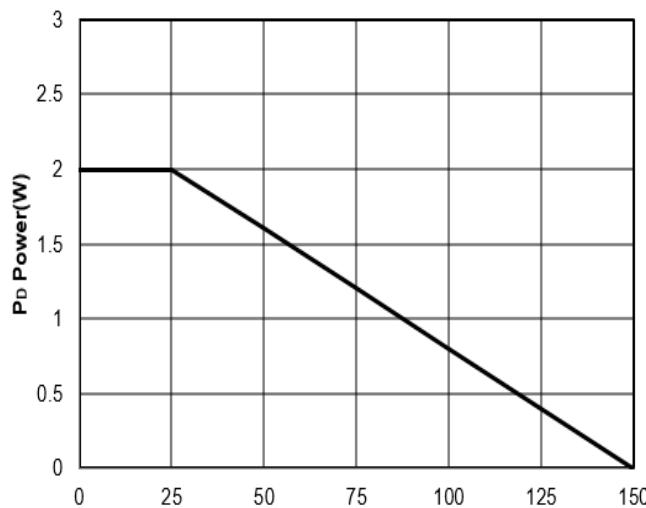
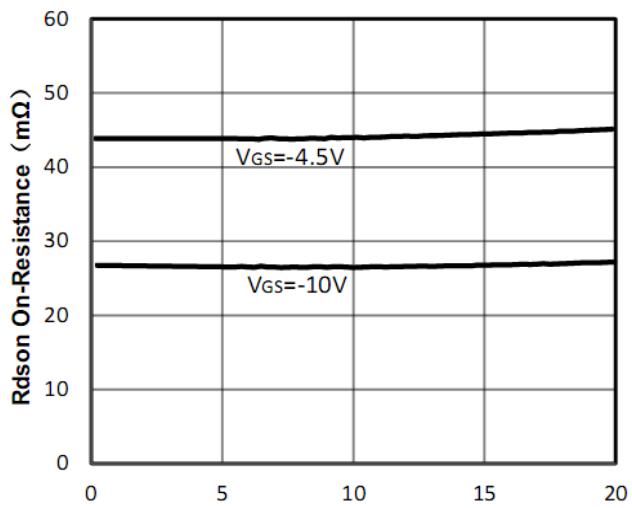
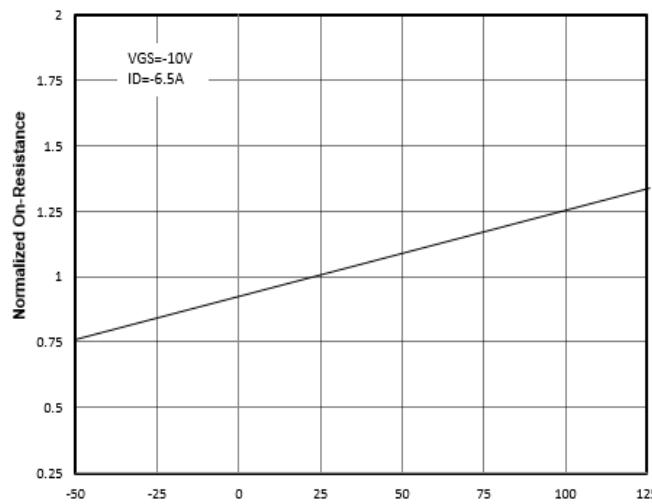
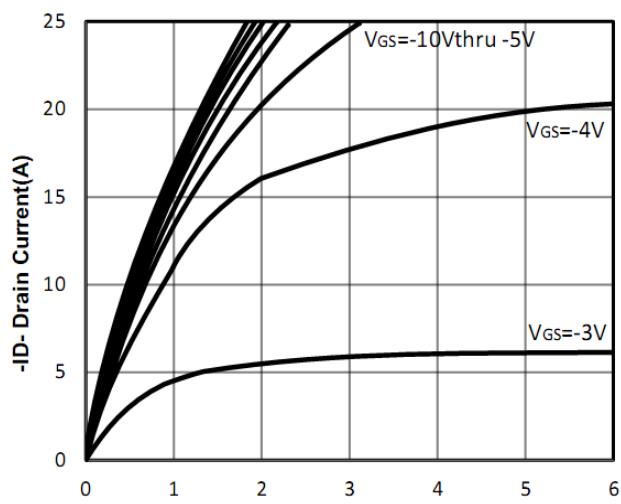
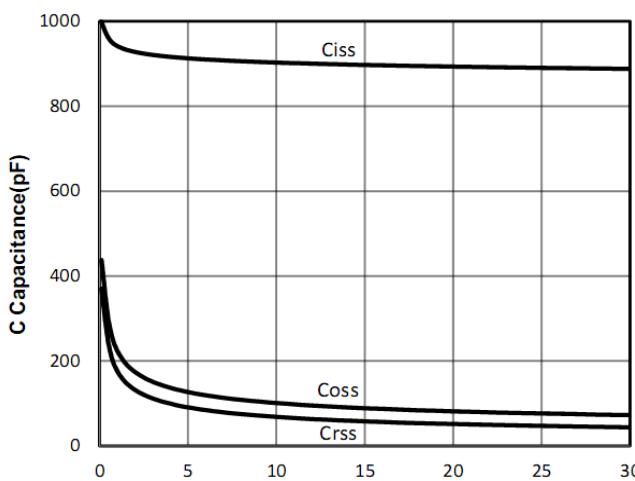
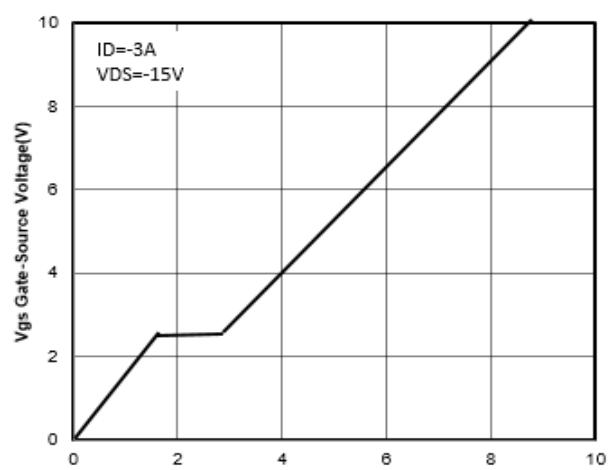
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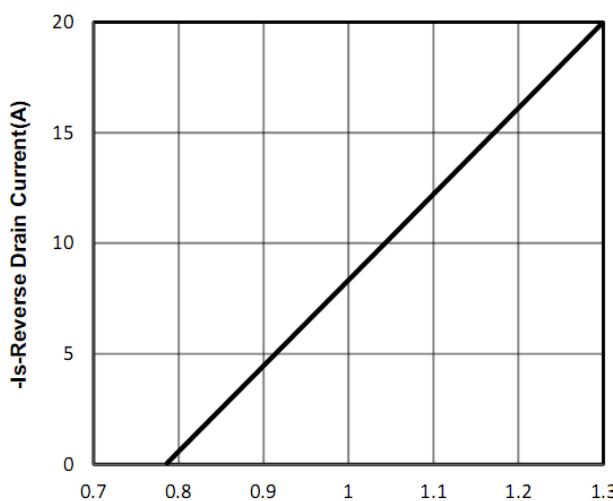
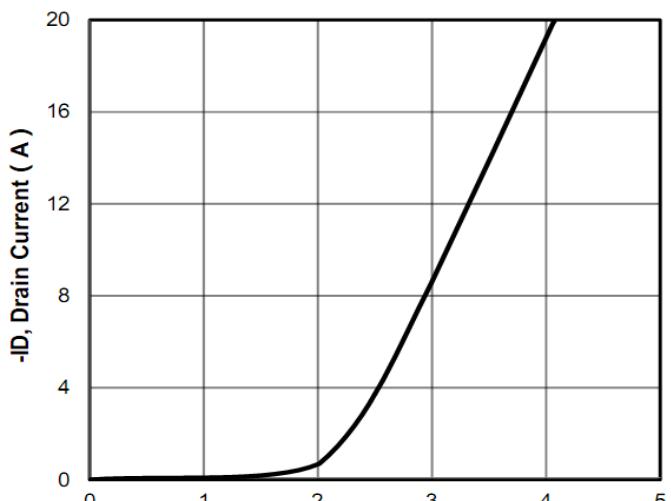
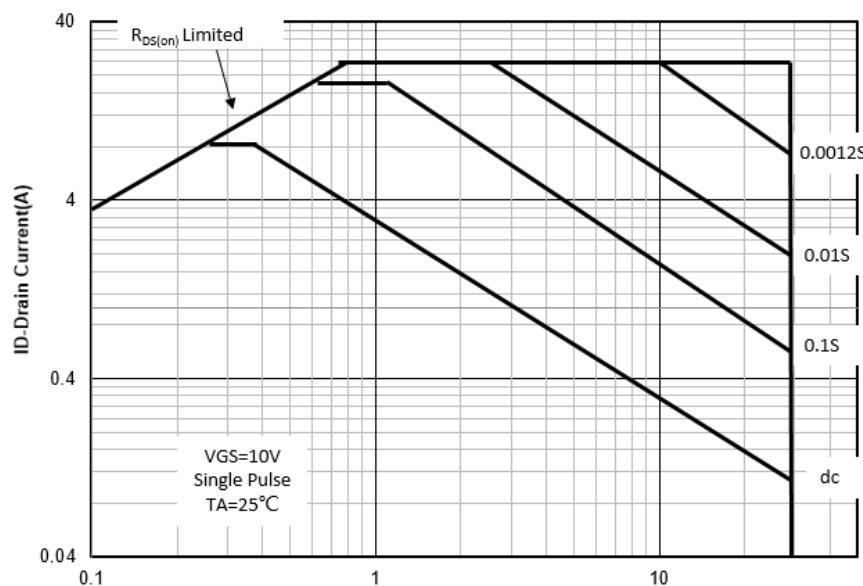
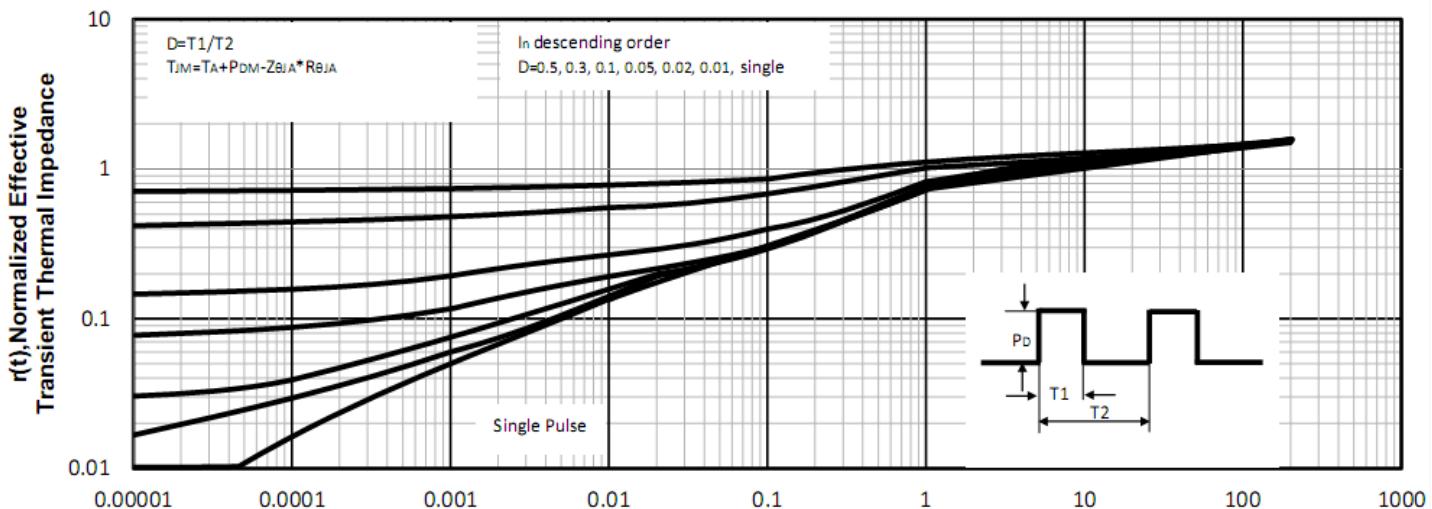
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec
3. Pulse Test: pulse width  $\leq 300$  us, duty cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production testing.

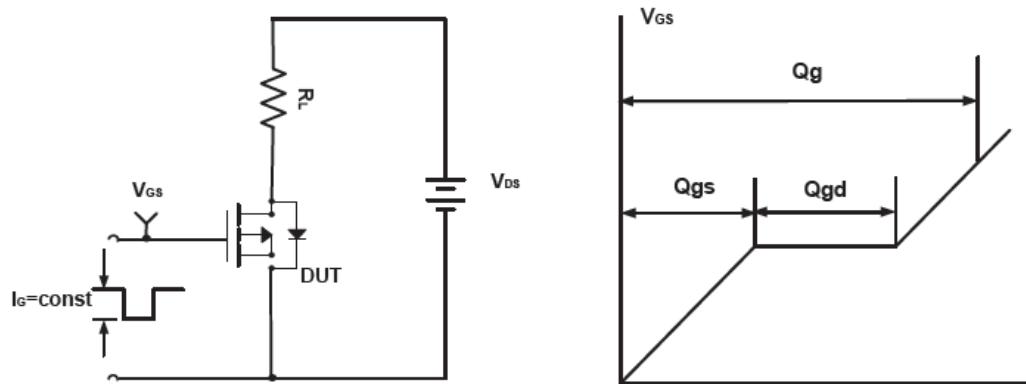
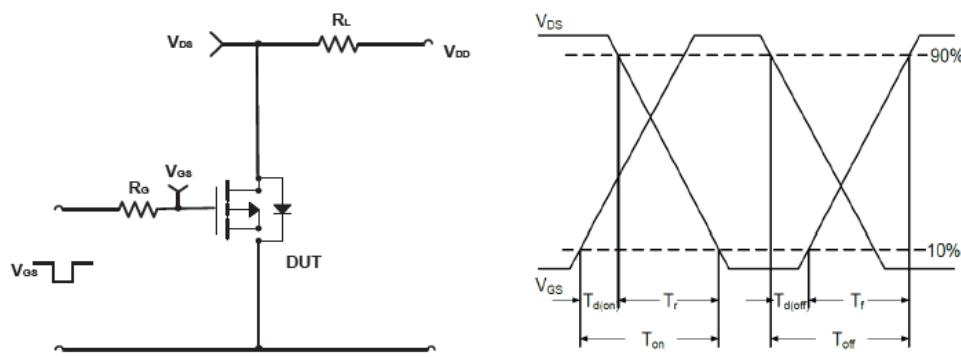
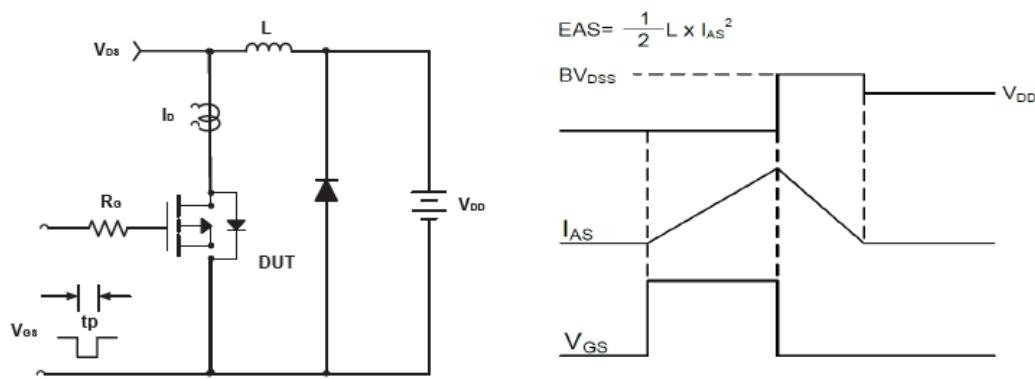
**Complementary High Density Trench MOSFET**
**Typical Characteristics(N-Channel)**

**Figure1:  $T_J$  Junction Temperature (°C)**

**Figure2:  $I_D$  Drain Current (A)**

**Figure3:  $T_J$  Junction Temperature (°C)**

**Figure4:  $V_{DS}$  Drain-Source Voltage (V)**

**Figure5:  $V_{DS}$  Drain-Source Voltage (V)**

**Figure6:  $Q_g$  Gate Charge (nC)**

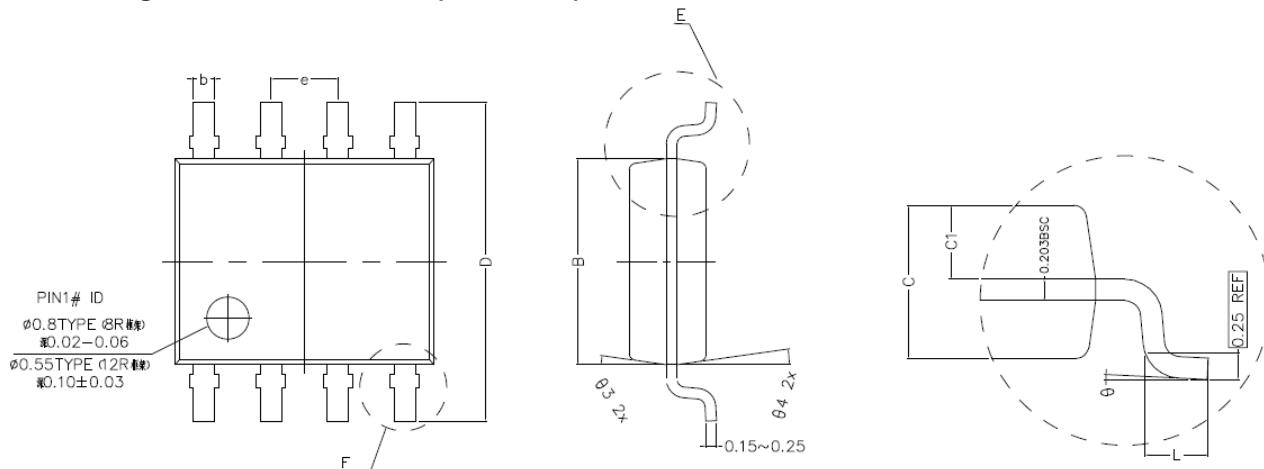
**Complementary High Density Trench MOSFET**

**Figure7: Vsd Source-Drain Voltage (V)**

**Figure8: Vgs Gate-Source Voltage (V)**

**Figure9: Vds Drain -Source Voltage (V)**

**Figure10: Square Wave Pulse Duration (sec)**

**Complementary High Density Trench MOSFET**
**Test Circuit and Waveform(N-Channel):**

**Figure A Gate Charge Test Circuit & Waveforms**

**Figure B Switching Test Circuit & Waveforms**

**Figure C Unclamped Inductive Switching Circuit & Waveforms**

**Complementary High Density Trench MOSFET**
**Typical Characteristics(P-Channel)**

**Figure11: TJ Junction Temperature (°C)**

**Figure12: -Id Drain Current (A)**

**Figure13: TJ Junction Temperature (°C)**

**Figure14: -Vds Drain-Source Voltage (V)**

**Figure15: -Vds Drain-Source Voltage (V)**

**Figure16: Qg Gate Charge (nC)**

**Complementary High Density Trench MOSFET**

**Figure17: -Vsd Source-Drain Voltage (V)**

**Figure18: -Vgs Gate-Source Voltage (V)**

**Figure19: -Vds Drain-Source Voltage (V)**

**Figure20: Square Wave Pulse Duration (sec)**

**Complementary High Density Trench MOSFET**
**Test Circuit and Waveform(P-Channel):**

**Figure D Gate Charge Test Circuit & Waveforms**

**FigureE Switching Test Circuit & Waveforms**

**Figure F Unclamped Inductive Switching Circuit & Waveforms**

**Complementary High Density Trench MOSFET**
**SOP-8 Package Outline Dimensions (Units: mm)**

**DETAIL E**
**DETAIL F**

COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A	4.800	4.900	5.000
B	3.800	3.900	4.000
C	1.350	1.450	1.550
C1	0.650	0.700	0.750
D	5.900	6.100	6.300
L	0.500	0.600	0.700
b	0.350	0.400	0.450
h	0.050	0.150	0.250
e	1.270 TYPE		
$\theta_1$	7° TYPE(8R)	12° TYPE(12R)	
$\theta_2$	7° TYPE(8R)	10° TYPE(12R)	
$\theta_3$	8° TYPE(8R)	12° TYPE(12R)	
$\theta_4$	8° TYPE(8R)	10° TYPE(12R)	
$\theta$	$0^\circ \sim 8^\circ$		