



## N And P- Channel Enhancement Mode Power MOSSFET

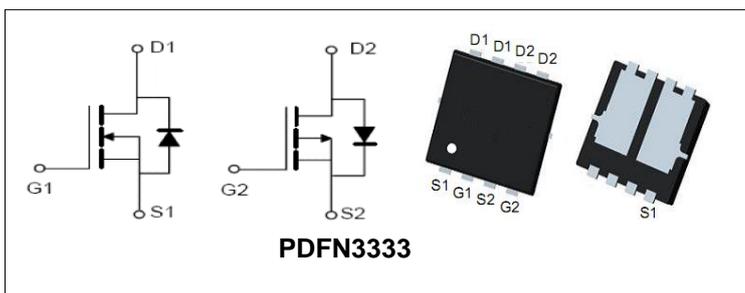
### Features

- Improved dv/dt Capability, High Ruggedness
- Maximum Junction Temperature Range (150°C)

### Applications

- DC Fan
- Brushless motor
- Optimized for Power Management Applications for Portable Products, such as H-bridge, Inverters C ar Charger and Others

N-Channel		
BVDSS	30	V
ID	25	A
RDSON@VGS=10V	8.6	mΩ
RDSON@VGS=4.5V	15	mΩ



P-Channel		
BVDSS	-30	V
ID	-25	A
RDSON@VGS=-10V	17	mΩ
RDSON@VGS=-5V	24	mΩ

### Order Information

Product	Package	Marking	Reel Size	Reel	Carton
PTQ25C03	PDFN3333	PTQ25C03	13inch	5000PCS	50000PCS

### Absolute Maximum Ratings

Symbol	Parameter		N-Channel	P-Channel	Unit
<b>Common Ratings (TC=25°C Unless Otherwise Noted)</b>					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage		30	-30	V
$V_{GS}$	Gate-Source Voltage		±20	±20	V
$T_J$	Maximum Junction Temperature		150		°C
$T_{STG}$	Storage Temperature Range		-50 to 150		°C
$I_S$	Diode Continuous Forward Current	TC =25°C	25	-25	A
<b>Mounted on Large Heat Sink</b>					
$I_{DM}$	Pulse Drain Current Tested (Silicon Limit) (Note1)	TC =25°C	75	-75	A
$I_D$	Continuous Drain current	TC =25°C	25	-25	A
$P_D$	Maximum Power Dissipation	TC =25°C	20		W
$R_{θJC}$	Thermal Resistance Junction-to-Case (Note2)		6.25		°C/W



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## N-Channel Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain- Source Breakdown Voltage	VGS=0V ID=250μA	30	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain current	VDS=30V,VGS=0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	VDS=VGS,ID=250μA	1	1.6	2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance (Note3)	VGS=10V, ID=20A	--	8.6	13	mΩ
		VGS=4.5V, ID=10A	--	15	18	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated) (Note4)</b>						
C <sub>iss</sub>	Input Capacitance	VDS= 15V, VGS=0V, F=1MHz	--	1060	--	pF
C <sub>oss</sub>	Output Capacitance		--	161	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	138	--	pF
Q <sub>g</sub>	Total Gate Charge	VDS= 15V, ID= 20A, VGS= 10V	--	12	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	3	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	4	--	nC
<b>Switching Characteristics (Note4)</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	VDD=15V, ID=20A, VGS=10V, RG=1.8Ω	--	6	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	11	--	nS
t <sub>d(off)</sub>	Turn-off Delay Time		--	12	--	nS
t <sub>f</sub>	Turn-off Fall Time		--	5	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage (Note3)	IS=20A,VGS=0V	--	0.82	1.2	V



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## P-Channel Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain- Source Breakdown Voltage	VGS=0V ID=-250μA	-30	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain current	VDS=-30V,VGS=0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	VDS=VGS,ID=-250μA	-1.0	-2	-2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance (Note3)	VGS=-10V, ID=-10.5A	--	17	22	mΩ
		VGS=-5V, ID=-6A	--	24	33	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated) (Note4)</b>						
C <sub>iss</sub>	Input Capacitance	VDS= -15V, VGS=0V, F=1MHz	--	1495	--	pF
C <sub>oss</sub>	Output Capacitance		--	166	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	150	--	pF
Q <sub>g</sub>	Total Gate Charge	VDS= -15V, ID= -15A, VGS= -10V	--	81	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	12	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	9.7	--	nC
<b>Switching Characteristics (Note4)</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	VDD=- 15V, ID=-15A, VGS=-10V, RG=3Ω	--	17	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	21	--	nS
t <sub>d(off)</sub>	Turn-off Delay Time		--	36	--	nS
t <sub>f</sub>	Turn-off Fall Time		--	15	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage (Note3)	IS=-20A,VGS=0V	--	-0.8	-1.2	V

Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec
3. Pulse Test: pulse width ≤ 300 us, duty cycle ≤ 2%.
4. Guaranteed by design, not subject to production testing.



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Typical Characteristics (N-Channel)

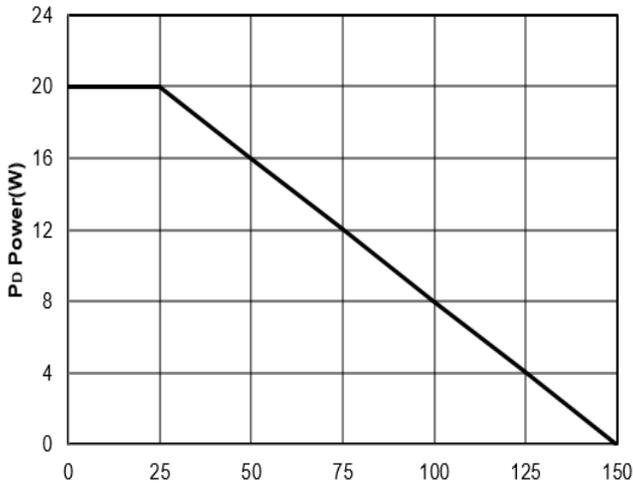


Figure1: T<sub>j</sub> Junction Temperature (°C)

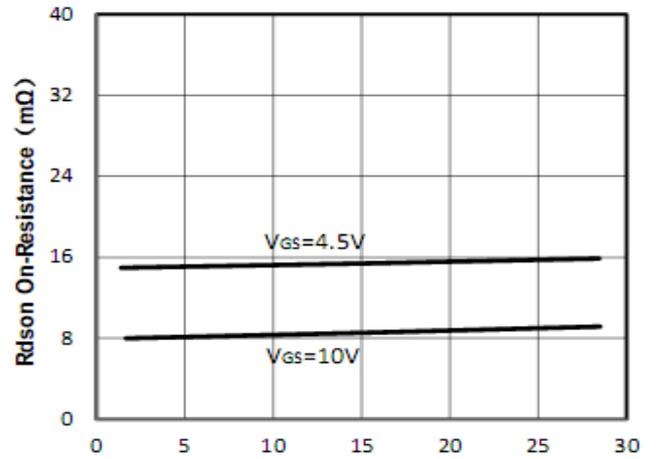


Figure2: I<sub>D</sub> Drain Current (A)

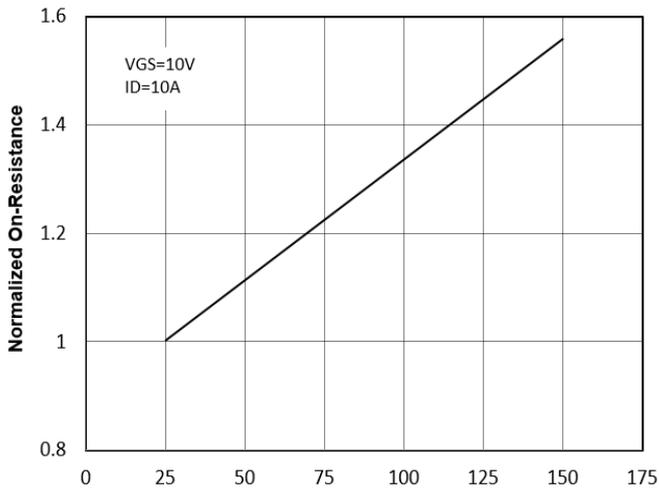


Figure3: T<sub>j</sub> Junction Temperature (°C)

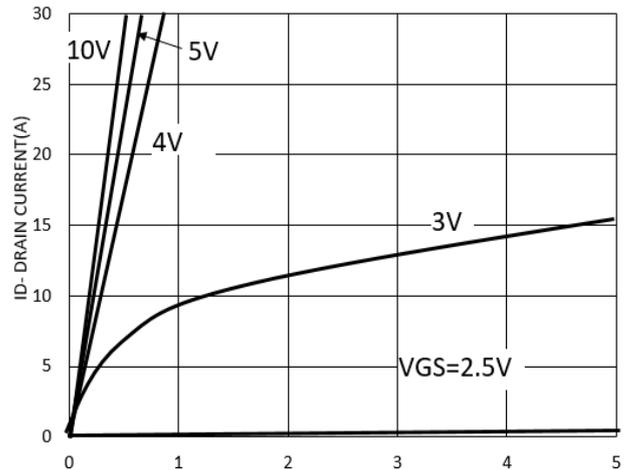


Figure4: V<sub>DS</sub> Drain-Source Voltage (V)

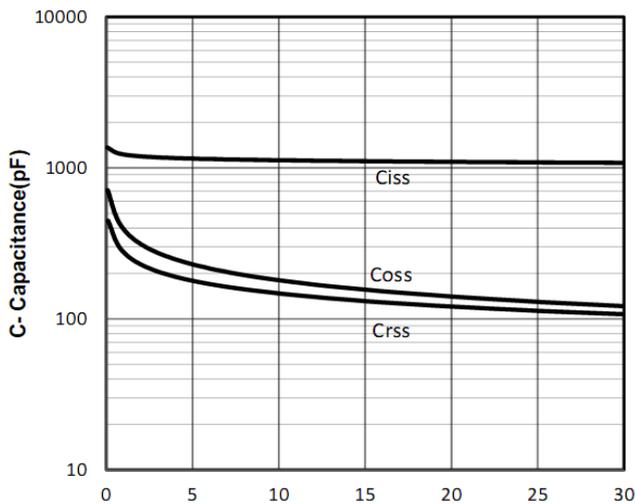


Figure5: V<sub>DS</sub> Drain-Source Voltage (V)

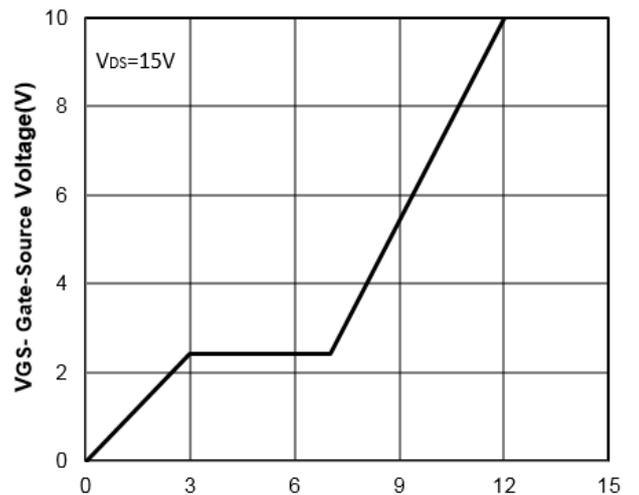


Figure6: Q<sub>g</sub> Gate Charge (nC)



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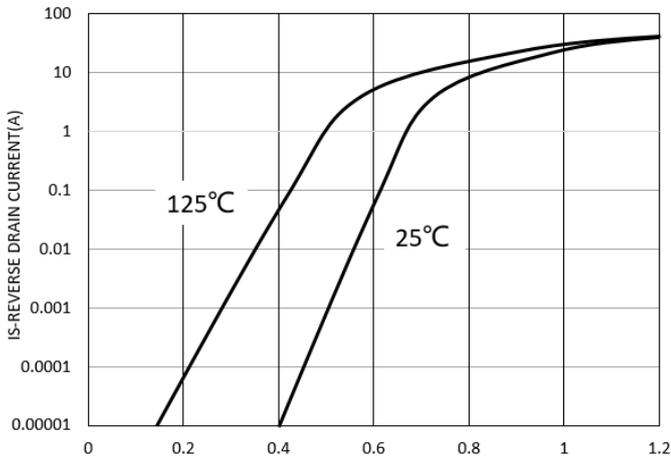


Figure7: Vsd Source-Drain Voltage (V)

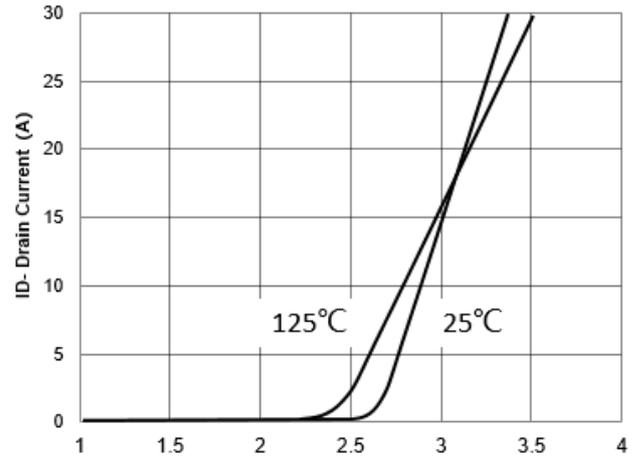


Figure8: Vgs Gate-Source Voltage (V)

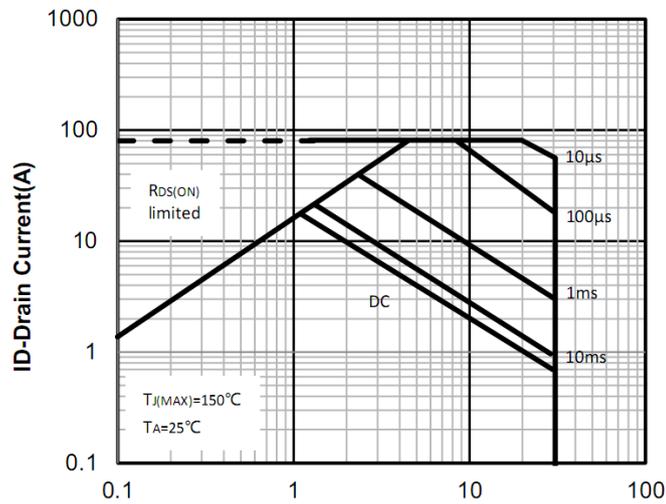


Figure9: VDS Drain -Source Voltage (V)

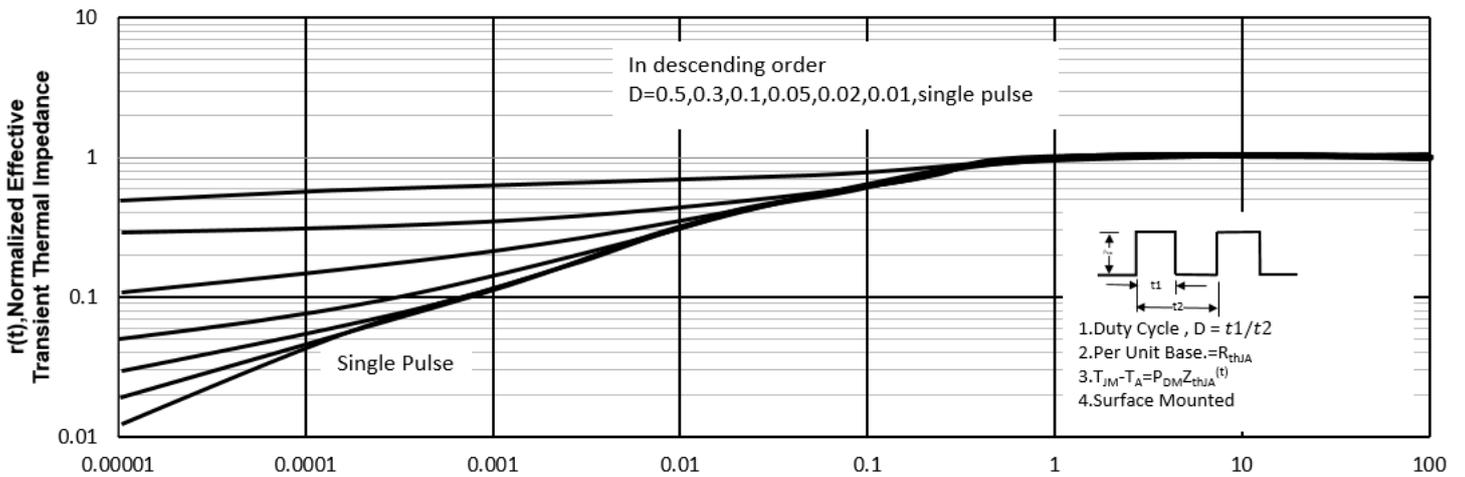


Figure10: Square Wave Pulse Duration (sec)

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Test Circuit and Waveform(N-Channel):

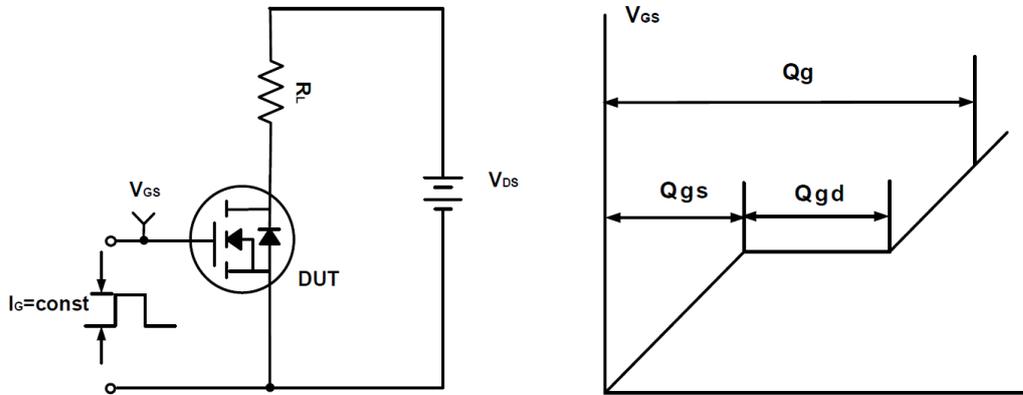


Figure A Gate Charge Test Circuit & Waveforms

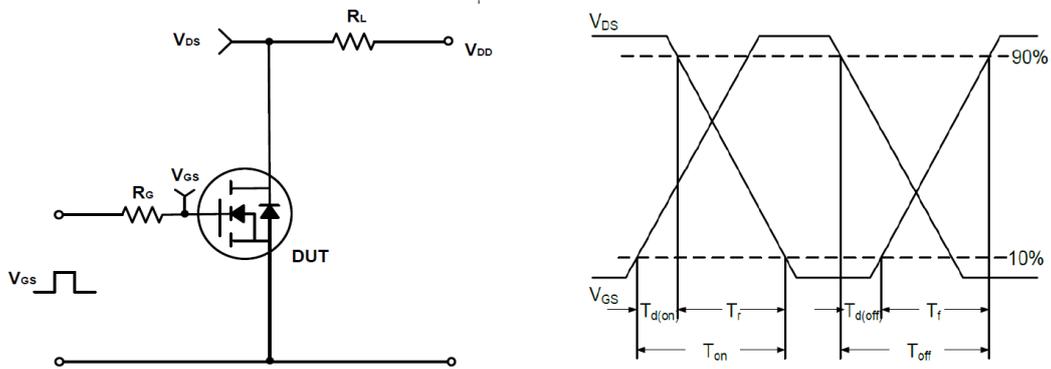


Figure B Switching Test Circuit & Waveforms



N And P- Channel Enhancement Mode Power MOSSFET

Typical Characteristics (P-Channel)

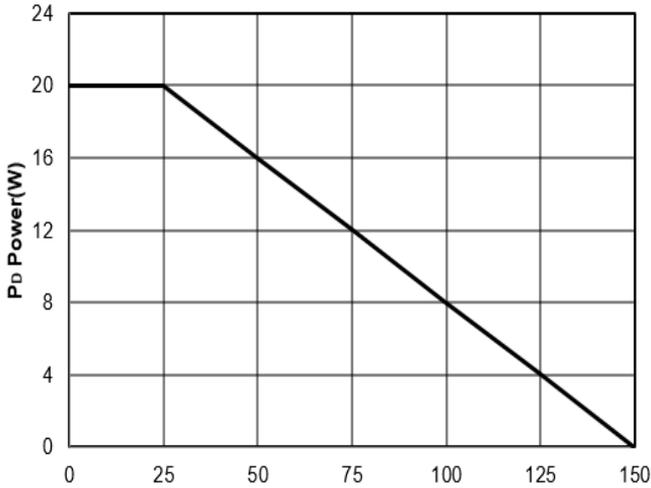


Figure11: T<sub>j</sub> Junction Temperature (°C)

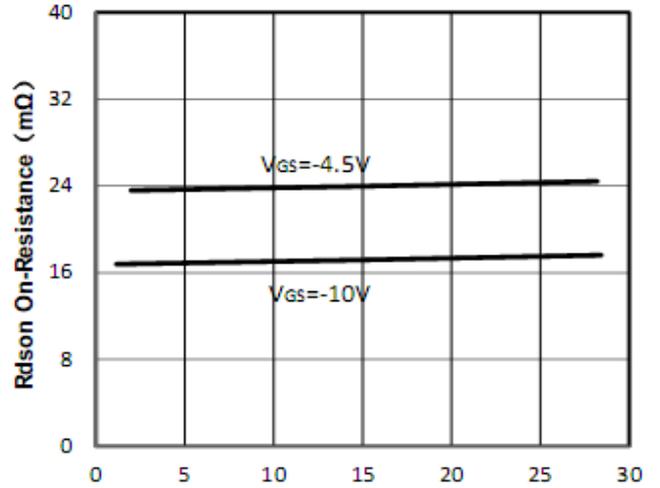


Figure12: -I<sub>d</sub> Drain Current (A)

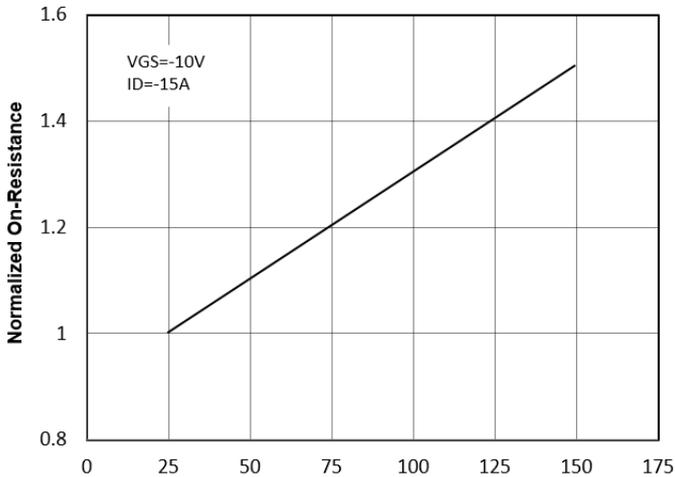


Figure13: T<sub>j</sub> Junction Temperature (°C)

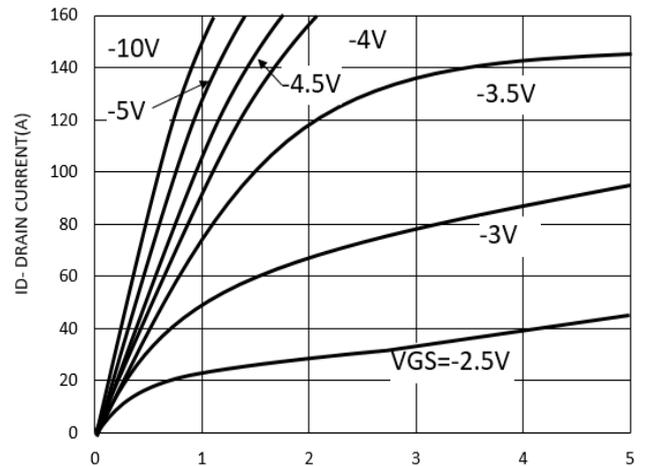


Figure14: -V<sub>ds</sub> Drain-Source Voltage (V)

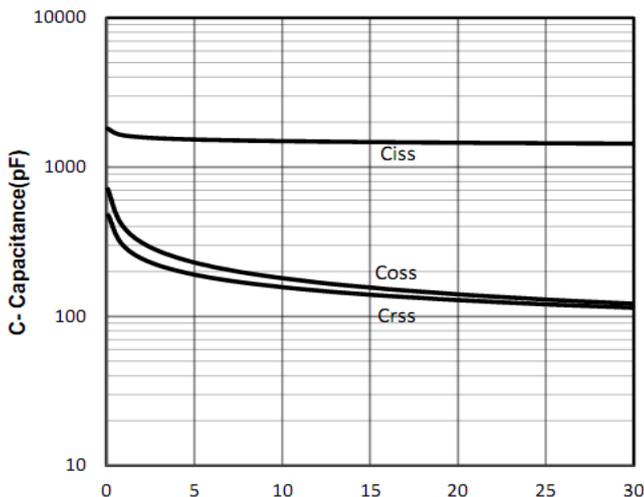


Figure15: -V<sub>ds</sub> Drain-Source Voltage (V)

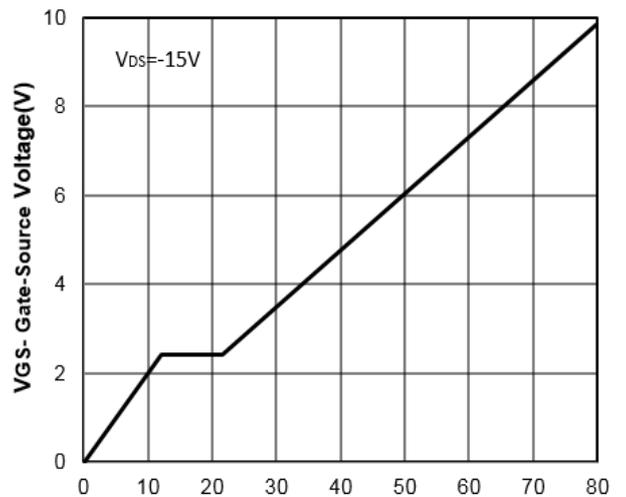


Figure16: Q<sub>g</sub> Gate Charge (nC)



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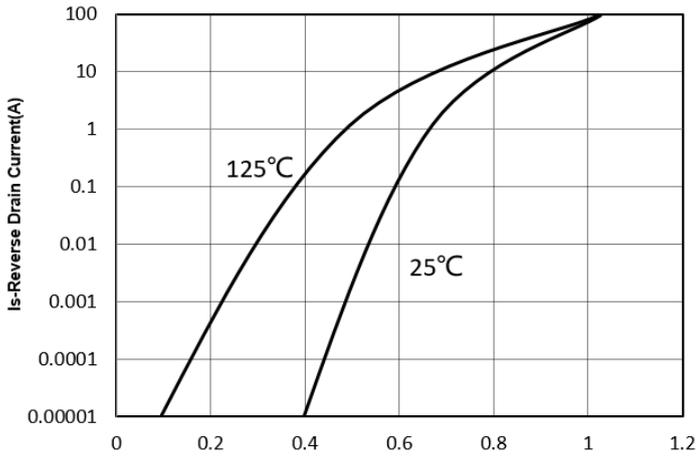


Figure17: -Vsd Source-Drain Voltage (V)

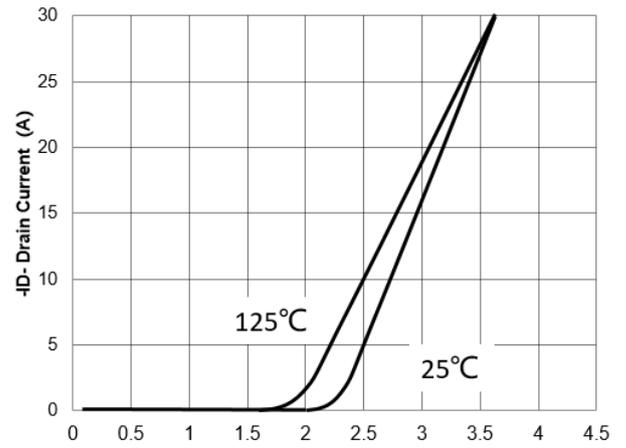


Figure18: -Vgs Gate-Source Voltage (V)

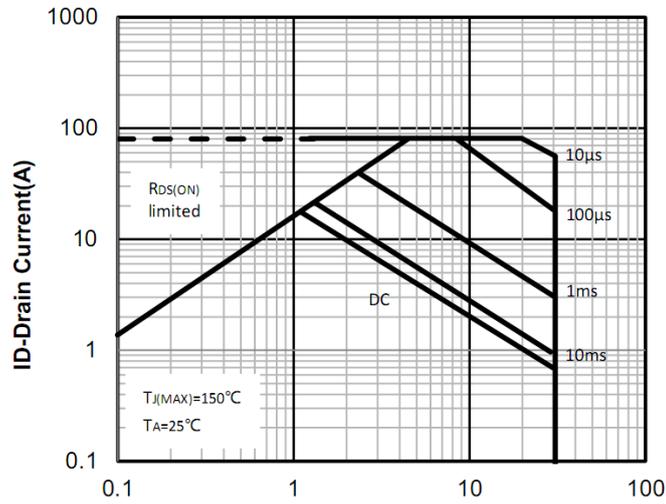


Figure19: -VDS Drain -Source Voltage (V)

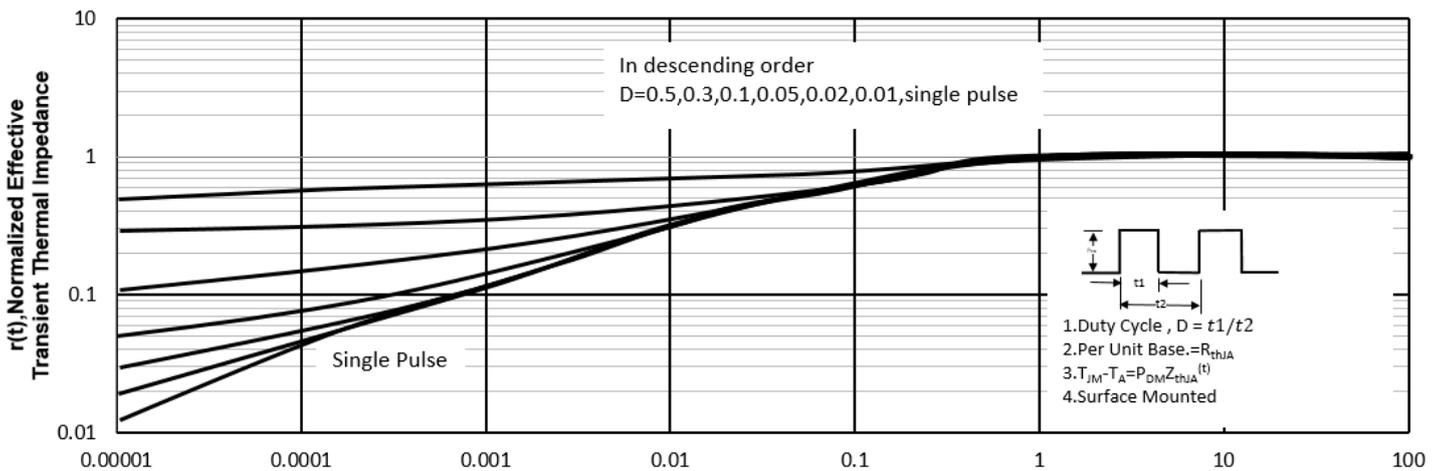


Figure20: Square Wave Pulse Duration (sec)

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Test Circuit and Waveform (P-Channel):

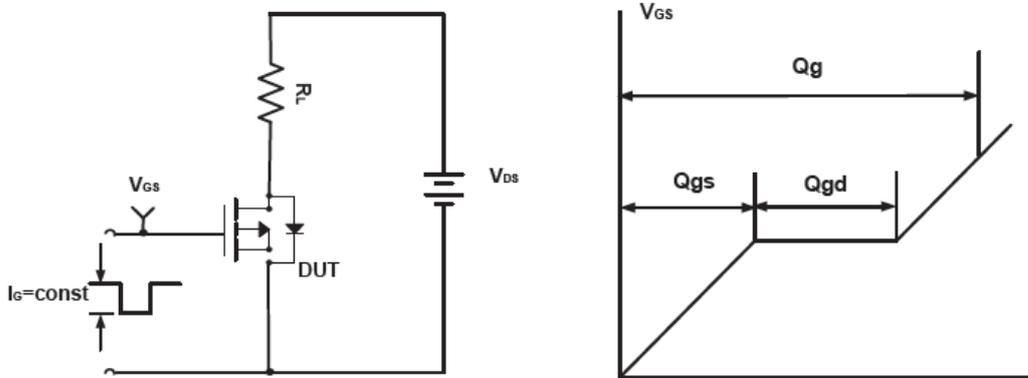
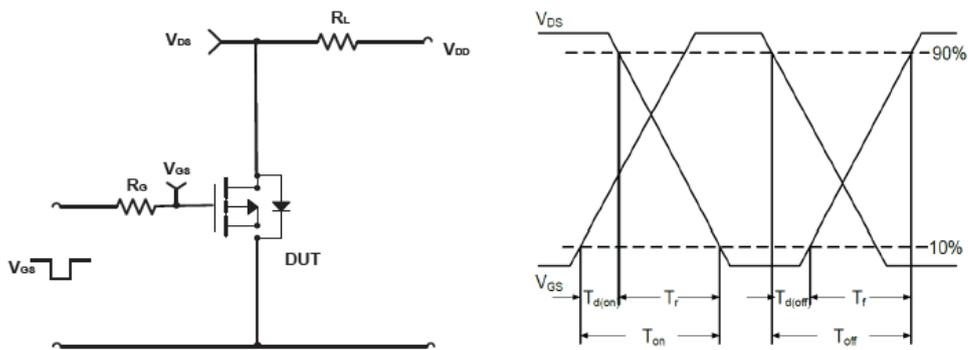
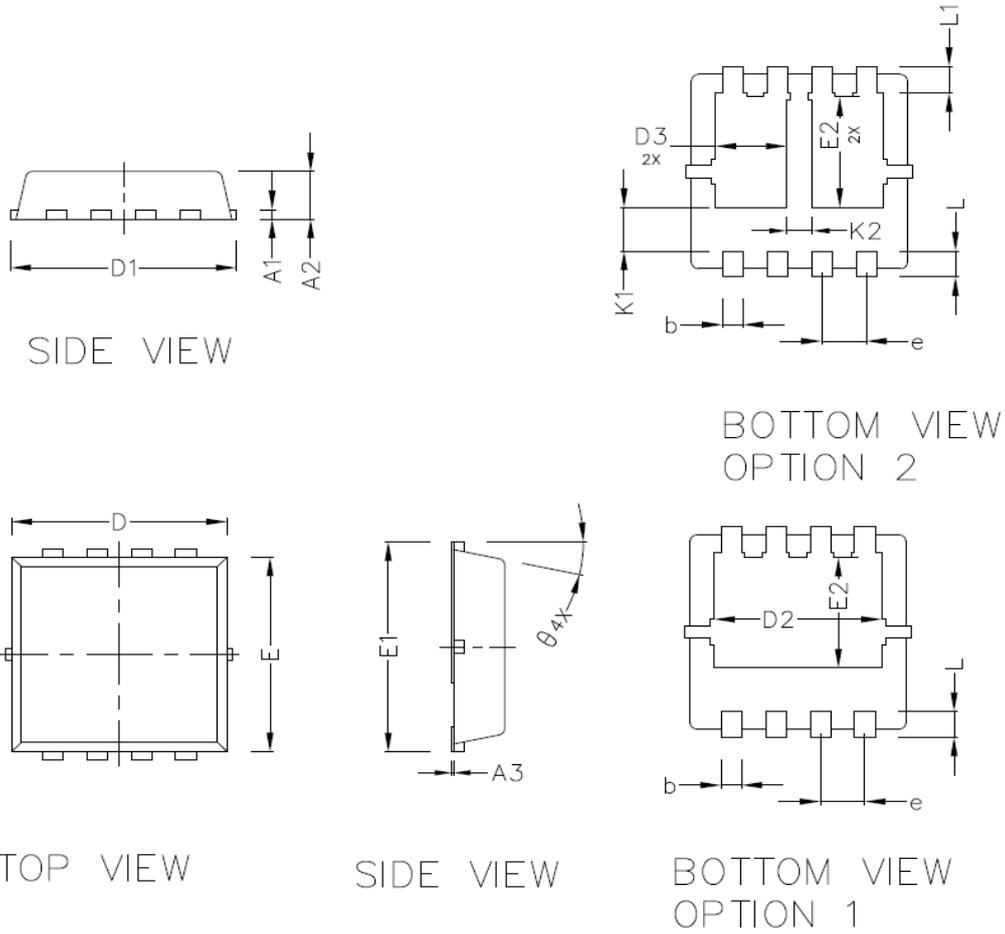


Figure D Gate Charge Test Circuit & Waveforms



FigureE Switching Test Circuit & Waveforms

**N And P- Channel Enhancement Mode Power MOSSFET**
**PDFN3333 Package Outline Dimensions (Units: mm)**


COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A1	0.152 BSC		
A2	0.650	0.750	0.850
A3	0.005	—	0.020
b	0.250	0.300	0.350
D	3.050	3.150	3.250
D1	3.200	3.300	3.400
D2	2.350	2.450	2.550
D3	0.935	1.035	1.135
E1	3.150	3.300	3.450
E	2.950	3.050	3.150
E2	1.635	1.735	1.835
e	0.650 TYPE		
L	0.300	0.400	0.500
θ	12° TYPE		
K1	0.680 REF		
K2	0.380 REF		
L1	0.410 REF		